

Notice of References Cited	Application/Control No. 09/800,493		Applicant(s)/Patent Under Reexamination MORIYA ET AL	
	Examiner Kevin Quinto		Art Unit 2826	Page 1 of 1

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	C	US-			
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	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, "Silicon Processing for the VLSI Era, Vol. 2 – Process Integration," 1990, Lattice Press, Vol. 2, p.194-196
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.